

特許協力条約に基づいて公開された国際出願

Figure 1 is a block diagram of a multi-channel communication system. On the left, an antenna (51) receives a signal (S10) and passes it to a demodulator (52). The demodulator (52) is part of a receiver section (50) and outputs a signal (S34) to a controller unit (54). The controller unit (54) is connected to a multiplexer (53) and four encoders (55, 56, 57, 58). The multiplexer (53) receives signals (S36, S37, S38, S39, S40, S41, S42) from the encoders and outputs a signal (S43) to a modulator (59). The modulator (59) is part of a transmitter section (60) and outputs a signal (S44) to an antenna (60). The receiver section (50) also includes a demodulator (52) and a controller unit (54). The transmitter section (60) includes a modulator (59) and an antenna (60). The system is controlled by a controller unit (54) which manages the encoding and multiplexing process.

52 ... demodulator
 53 ... multiplexer
 54 ... controller unit
 55-58 ... encoder
 59 ... modulator